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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/670,154	09/26/2000	Gengying Gao	75292/06037	4842	
759	00 12/18/2002				
JURGEN K. VOLLRATH			EXAMINER		
588 Sutter Street #531			NGUYEN, TRUNG Q		
San Francisco, CA 94102			ART UNIT	PAPER NUMBER	
			2829		
			DATE MAILED: 12/18/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No. Application No. GAO ET AL.	•				V _A			
Examiner Trung Q Nguyen 2829		Applic	eation No.	Applicant(s)				
Period for Reply. A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply. A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. If the period for reply specified above is less than thiny (30) eays, a reply within the statutory minimum of thiny (30) days, will be considered timely. If the period for reply specified above is less than thiny (30) eays, a reply within the statutory minimum of the mailing date of this communication. If the period for reply specified above is less than thiny (30) eays, a reply with the statutory minimum of the mailing date of this communication. If the period for reply specified above is less than thiny (30) eays, and seed that the mailing date of this communication. (30) days will be considered timely. If the period for reply specified above is the statutory period will apply and led legis (30) days will be considered timely. If the period for reply specified above is the statutory period will apply and very ledice any statutory period will apply and very ledice any statutory period will apply and very ledice any search apply and very ledice and search apply and very ledice and search apply and very ledice and search and very ledice and search apply and very ledice and search apply and very ledice			0,154	GAO ET AL.				
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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Paniccia (U.S. 5,872,360).

As to claim 1, Paniccia et al. (hereafter Paniccia) disclose in Figure 4 a method of testing the ESD performance of an IC 405 device comprising a laser beam 413 and monitoring via detector 417 the amount of light reflected from the device (see column 4 line 61 to column 5 line 6).

As to claim 2, Paniccia discloses in Figure 4 a laser beam 413 is used to probe the IC device 405.

As to claim 3, Paniccia discloses in column 5, lines 24-31 that the energy of the laser beam corresponds substantially to the band gap of the substrate of the device.

As to claim 4, Paniccia discloses in column 5, lines 8-10 that the substrate is silicon and in column 5, lines 20-23 that the energy of the laser beam is about 1.1eV

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(via λ =1.064nm in wavelength, base on λ =E/hc, H is Plank constant (h=6.626x10⁻³⁴) and c is the speed of light (c=3x10⁸m/s therefore, E=1.1eV).

As to claim 5, Paniccia discloses in Figure 6 and in column 6, lines 6-14 that the diffusions of the IC device 603 are probed with the laser beam 609.

As to claim 6, Paniccia discloses in Figure 4 and in column 6, lines 13-17 that the device is probed through the back of the device.

As to claim 7, Paniccia discloses in Figure 4 and in column 5, lines 42-58 that the diffusions of I/O cells via P-N Junction of 603 are probed via light 609 to determine how much light is absorbed via light 413 of Fig. 4 and how much light is reflected via light 415 of Fig. 4 by the diffusions 58.

As to claim 8, Paniccia discloses in Figure 7 and in column 7, lines 1-23 that several samples via 703, 705 and 707 are taken of each probed location and the results averaged.

As to claims 9 and 17, Paniccia discloses in Figure 4 and in column 5, lines 20-23 that a mode-locked laser 407 is used to probe the IC device.

As to claims 10 and 18, Paniccia discloses in Figure 7 and in column 8, lines 6-15 that a continuous wave laser is used to probe the IC device. As to claims 11 and 19, Paniccia discloses in Figure 4 the mode locked laser 407 is positioned to provide a laser beam 413, which is focused on an active region 403. It is obvious that the mode locked laser 407 had positioned by a user in order to focused on an active region 403.

As to claims 12 and 20, Paniccia discloses in Figure 8 the mode-locked laser 803 is positioned automatically using image recognition 843 via camera.

As to claim 13, Although Paniccia does not explicitly disclose that power is supplied to the DUT; the power supply is inherent to the DUT since it requires power to operate.

As to claims 14-15, Paniccia discloses in Figure 4 testing is performed on the device in a package form (column 5, lines 9-10) and prepackage (via to be packaged, column 1, lines 50-55).

As to claim 16, Paniccia discloses the device includes only some of its layers (column 1, lines 62-67).

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Response to Arguments

- 3. Applicant's arguments with respect to claims 1-20 have been considered. Some of the arguments are most in view of the new explanations provided in the rejection for applicant's benefit. The other arguments are not persuasive.
- 4. The applicants argue that:
 - a) Paniccia does not disclose a method of testing the ESD performance of an integrated circuit (IC).
 - b) Paniccia does not disclose using a continuous laser to position the mode-locked laser.
 - c) Paniccia does not disclose testing a device having only some of its layers.
- 5. The examiner respectfully disagree to the above argues because:

In response to the argument a) above, it is known to all those skilled in the art that testing the ESD is known to apply "a light source via laser beam or the like, wherein the energy of the beam corresponds to the band-gap of the substrate of the DUT and monitoring the reflected by the diffusions from the beam to calculated the degree of electro-absorption in which related to the ESD susceptibility of the DUT." The cited reference (Paniccia et al.) disclose in Figure 4 a method of testing the ESD performance of an IC 405 device comprising a laser beam 413 and monitoring via detector 417 the amount of light reflected from the device (see column 4 line 61 to column 5 line 6). Therefore, the above argument is not persuasive

In response to the argument b) and c) above, Paniccia et al. disclose Paniccia discloses the device includes only some of its layers (column 1, lines 62-67) and in

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Figure 7 and in column 8, lines 6-15 that a continuous wave laser is used to probe the IC device. It is well known in the semiconductor industry that a continuous laser has a continuous wave length. Therefore, the above arguments are not persuasive

In addition, any special structural and/or functional aspect of the Applicant's invention that renders it patentably distinct over prior art has to be clear from the claims themselves; the Applicant cannot rely on the specification or on arguments that he advances to the Examiner in an amendment. That is, he cannot rely on factors not recited in the rejected claims. Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See In re Van Geuns, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993). See also Constant v. Advanced Micro-Devices, Inc., 848 F.2d 1560, 1571-72 7 USPQ2d 1057, 1064-1065 (Fed. Cir.), cert denied, 488 U.S. 892 (1988), in which it was held that various limitations on which the appellant relied could not be given meaning since they were not stated in the claims. In Ex parte McCullough, 7 USPQ2d 1889, 1891 (Bd. Pat. App. & Inter. 1987), a claimed electrode was rejected although it was asserted in the specification that it functions differently from prior art electrodes, since "although the demonstrated results may be germane to the patentability of a battery containing appellant's electrode, they are not germane to the invention claimed on appeal" (emphasis added).

Conclusion

6. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within two months of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trung Nguyen whose telephone number is 703-305-4925. The examiner can normally be reached on Monday through Friday, 8:30AM – 5:00PM. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-5841. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Cuneo Kammie can be reached at (703) 308-1233.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)-308-0956.

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

TN December 12, 2002